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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/824,639	04/15/2004	Mitsuo Umemoto	492322017800	7675
25227	7590	05/12/2005	EXAMINER	
MORRISON & FOERSTER LLP 1650 TYSONS BOULEVARD SUITE 300 MCLEAN, VA 22102			VU, DAVID	
			ART UNIT	PAPER NUMBER
			2818	

DATE MAILED: 05/12/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

**Office Action Summary**

Application No.

10/824,639

Applicant(s)

UMEMOTO ET AL.

Examiner

DAVID VU

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

**Period for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

**Status**

- 1) ☒ Responsive to communication(s) filed on 15 April 2004.
- 2a) ☐ This action is FINAL. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

**Disposition of Claims**

- 4) ☒ Claim(s) 1-7 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1-7 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

**Application Papers**

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 15 April 2004 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

**Priority under 35 U.S.C. § 119**

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some \* c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- \* See the attached detailed Office action for a list of the certified copies not received.

**Attachment(s)**

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftperson's Patent Drawing Review (PTO-948)
- 3) ☒ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)  
Paper No(s)/Mail Date 06/17/04&01/19/05.
- 4) ☐ Interview Summary (PTO-413)  
Paper No(s)/Mail Date. \_\_\_\_\_.
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: \_\_\_\_\_.

## DETAILED ACTION

### Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

1. Claims 1-3 are rejected under 35 U. S. C. 102(b) as being anticipated by Applicant admitted Prior Art (AAPA) (See US 2005/0003576A1).

AAPA, in related text and figures (figs. 3A-3B) discloses a method of manufacturing a semiconductor device, comprising: providing a substrate 2 having a first electrode 3 thereon; dispensing a sealing resin 4 to a region of the substrate 2 that does not include the first electrode 3; providing a semiconductor chip 6 having a second electrode 7 on a peripheral portion of a front surface of the semiconductor chip 6; placing the semiconductor chip 6 over the substrate 2 so that the front surface of the semiconductor chip 6 faces the sealing resin 4; applying a pressure to a peripheral portion of a back surface of the semiconductor chip (press the whole surface of the semiconductor chip 6) so that the first and second electrodes come into a contact (see [0007]); and applying a pressure to a central portion of the back surface of the

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semiconductor chip (press the whole surface of the semiconductor chip 6) so that the sealing resin extends in a space between the substrate and the front surface of the semiconductor chip (see [0007]).

2. Claims 1-7 are rejected under 35 U. S. C. 102(e) as being anticipated by Ikegami (US Pat 6,674,178).

Ikegami discloses in figs. 6-9 a method of manufacturing a semiconductor device, comprising: providing a substrate 15 having a first electrode 16 thereon; dispensing a sealing resin 17 to a region of the substrate 15 that does not include the first electrode 16; providing a semiconductor chip 11 having a second electrode 13' on a peripheral portion of a front surface of the semiconductor chip 11; placing the semiconductor chip 11 over the substrate 15 so that the front surface of the semiconductor chip 11 faces the sealing resin 17; applying a negative pressure (air suction) to a central portion of a back surface of the semiconductor chip (col. 7, line 63 through col. 8, line 23) and a positive pressure (pressing the semiconductor chip 11 with a fixed amount of force) to a peripheral portion of the back surface of the semiconductor chip so that the first and second electrodes come into a contact; and applying a positive pressure to the central portion of the back surface of the semiconductor chip so that the sealing resin extends in a space between the substrate and the front surface of the semiconductor chip (col. 8, lines 24-43).

### **Conclusion**

3. Any inquiry concerning this communication or earlier communications from the examiner should be directed to David Vu whose telephone number is (571) 272-1798. The examiner can normally be reached on Monday-Friday from 8:00am to 5:00pm. If attempt to reach the examiner by telephone are unsuccessful, the examiner's supervisor, David Nelms can be reached on (571) 272-1787. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR, Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



David Vu

May 10, 2005.